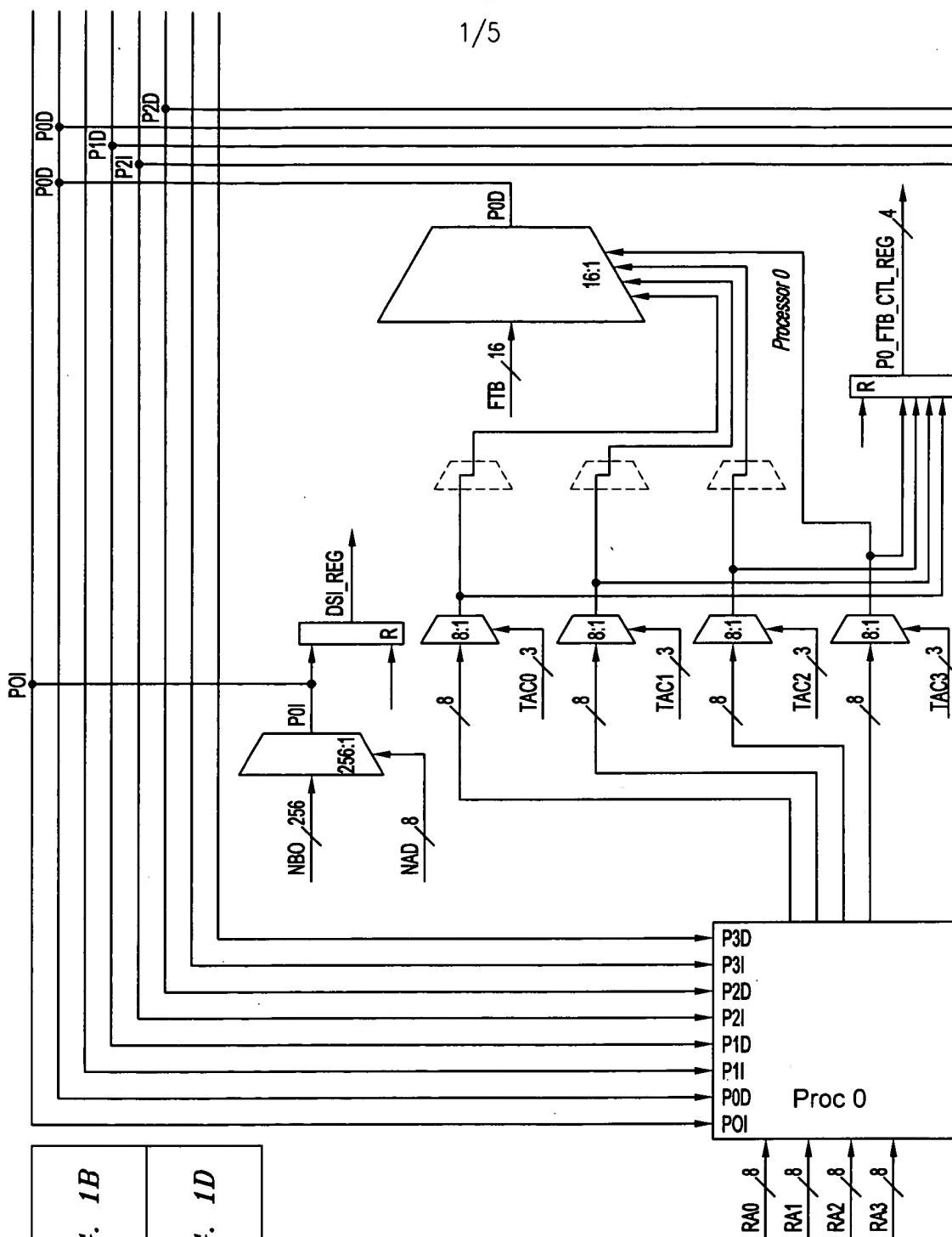


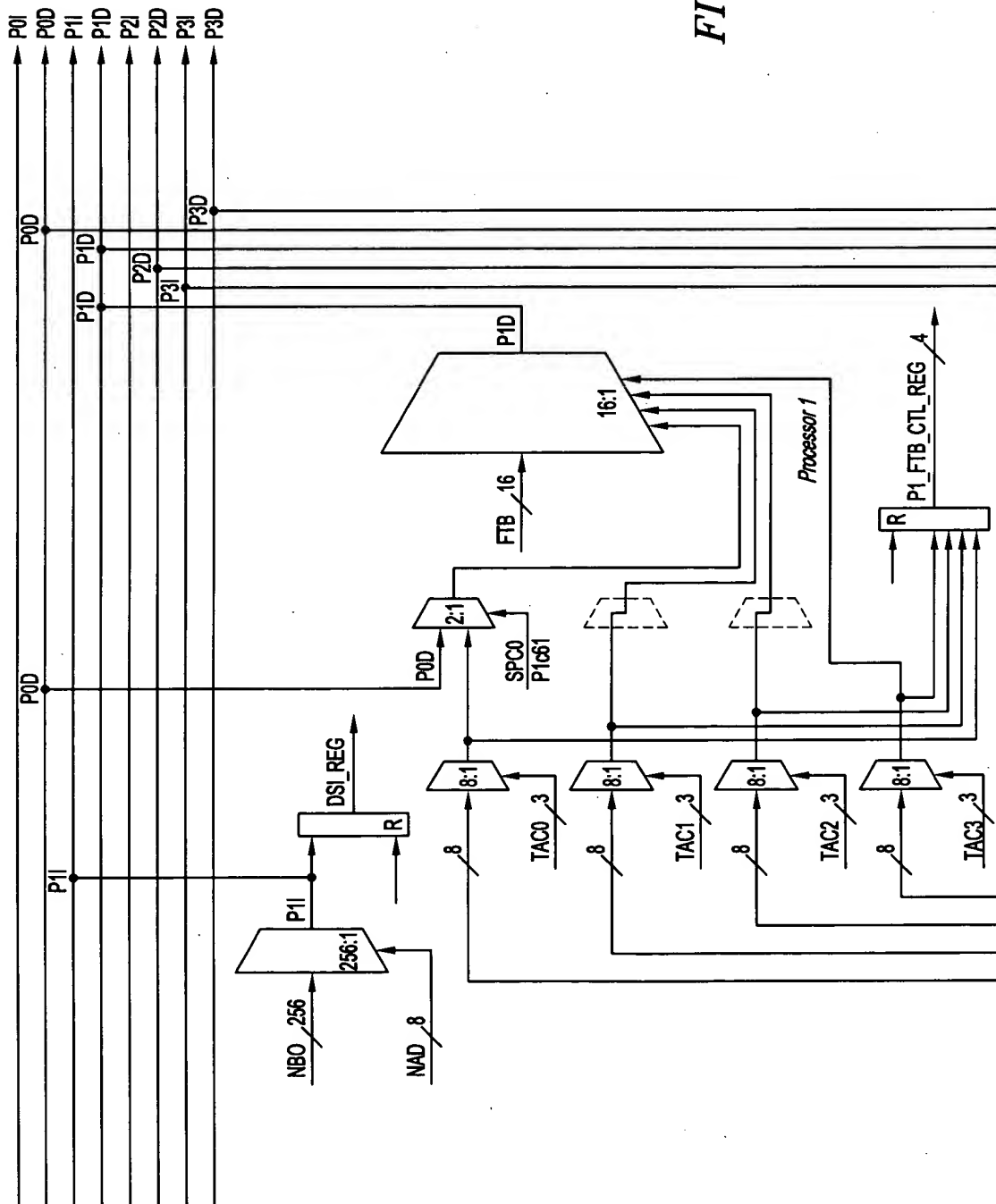
HIGH SPEED SOFTWARE DRIVEN EMULATOR COMPRISED OF A PLURALITY OF
EMULATION PROCESSORS WITH IMPROVED MULTIPLEXED DATA MEMORY
INV.: WILLIAM F. BEAUSOLEIL
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 $\frac{1}{5}$ 

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FIG. 1B



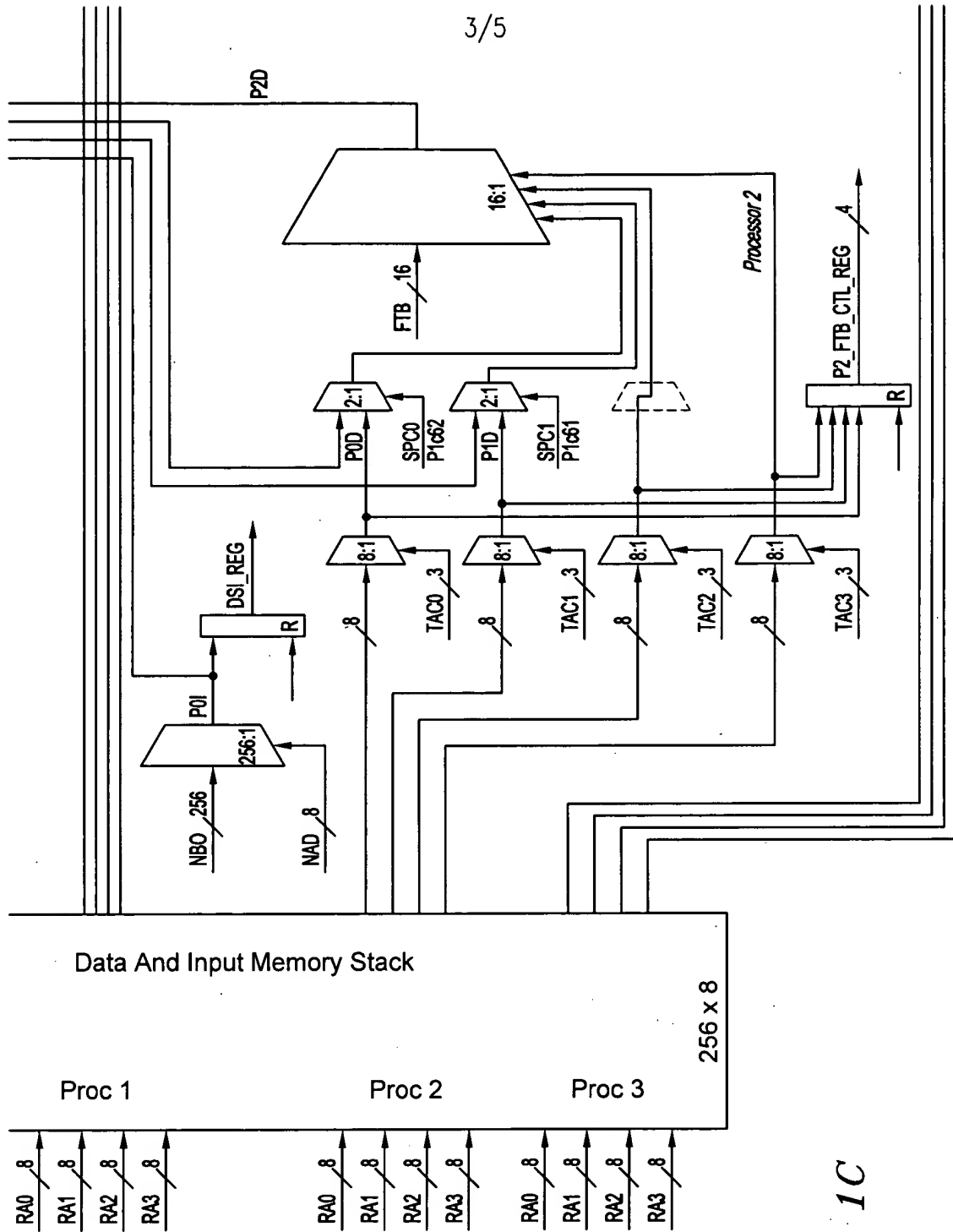


FIG. 1C

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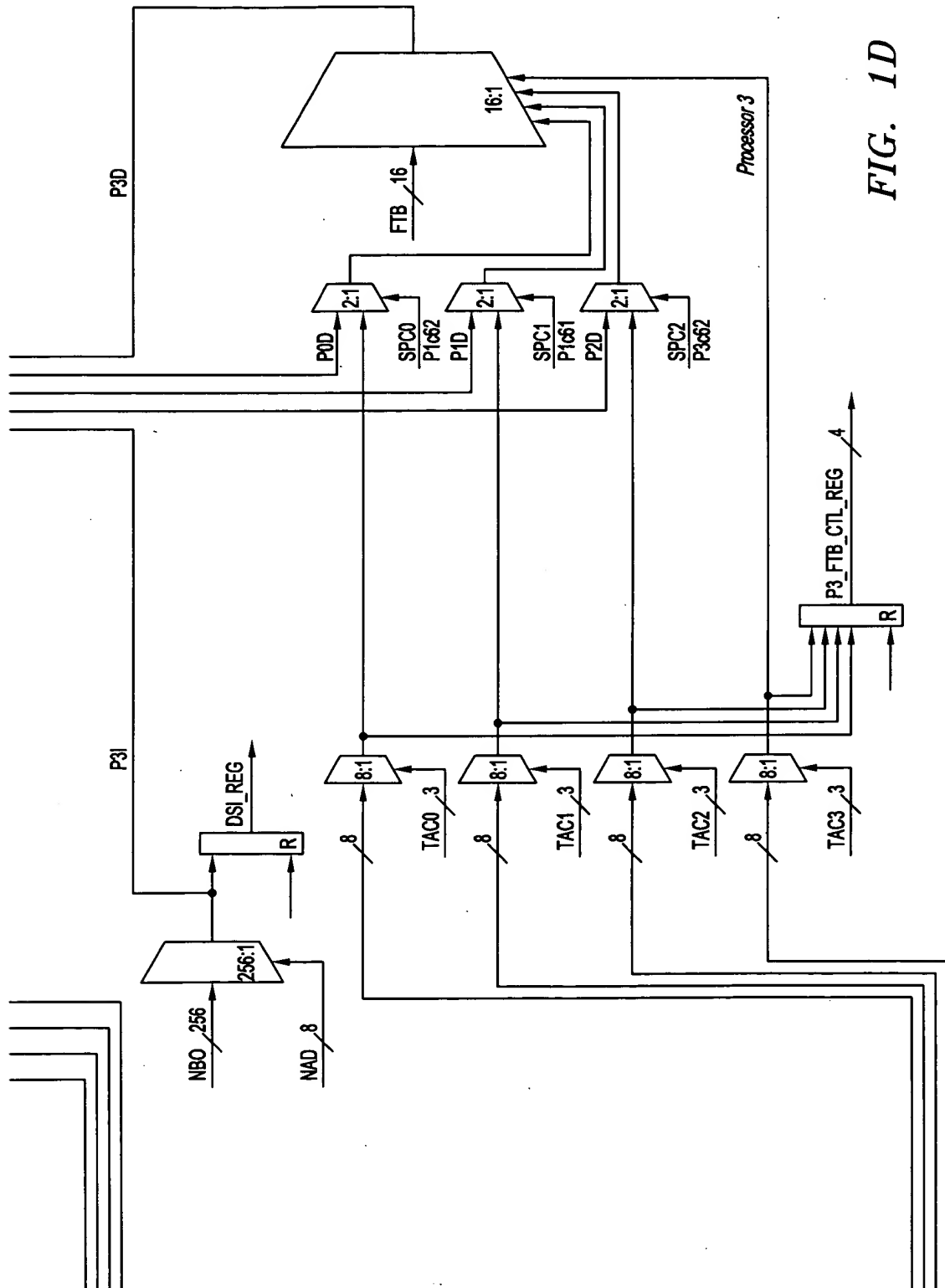


FIG. 1D

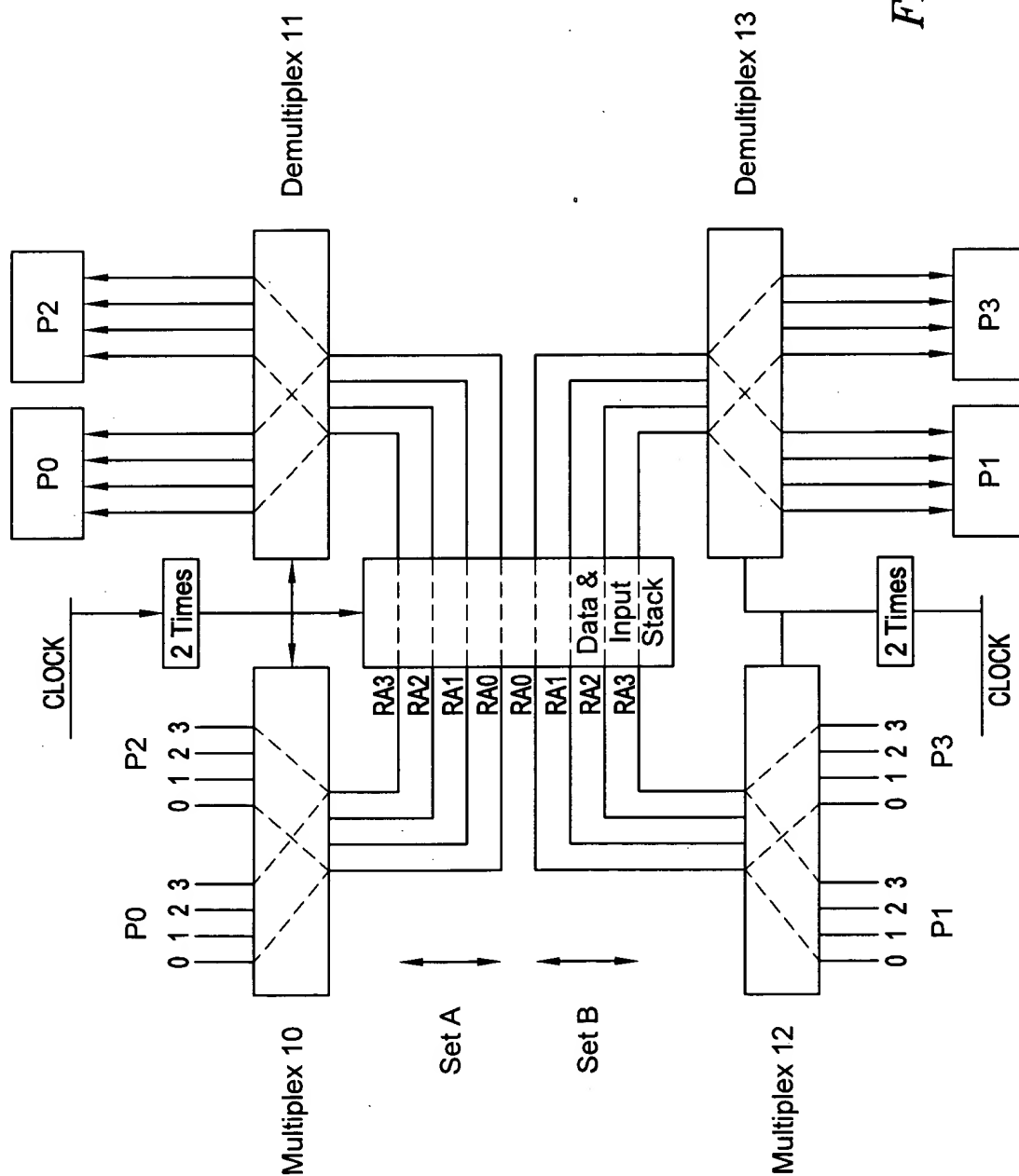


FIG. 2